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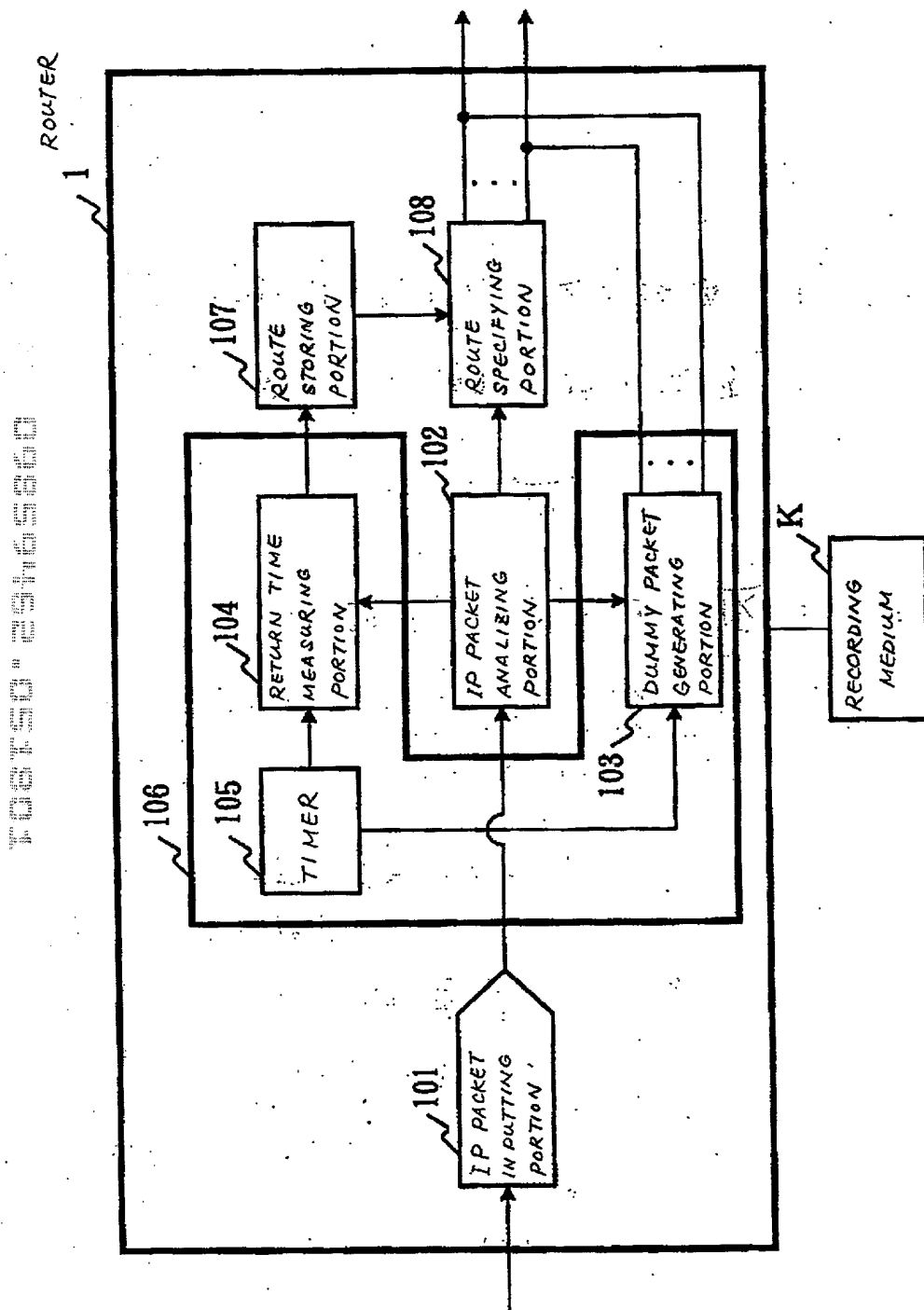


FIG. 1

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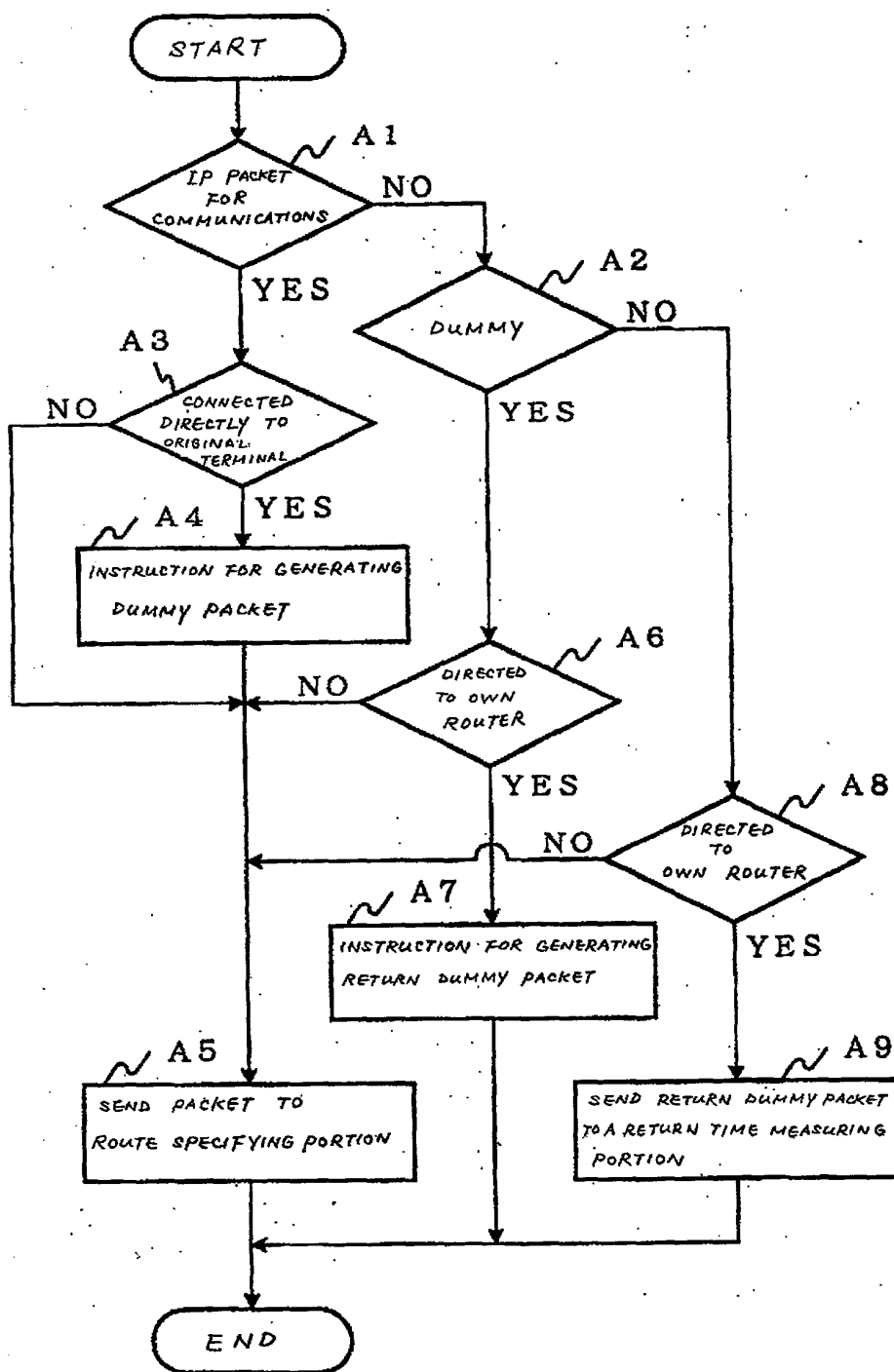


FIG. 2

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0	1	2	3	4	5	6	7
PRECEDENCE			D	T	R	0	0

Bits 0-2 : Precedence, 0 (LOWER PRECEDENCE) ~ 7 (HIGHER PRECEDENCE).

- | | |
|------------------------------------|-----------------------|
| Bit 3 : 0 = Normal Delay, | 1 = Low Delay. |
| Bit 4 : 0 = Normal Throughput, | 1 = High Throughput. |
| Bit 5 : 0 = Normal Reliability, | 1 = High Reliability. |
| Bit 6,7 : Reserved for Future Use. | |

FIG. 3

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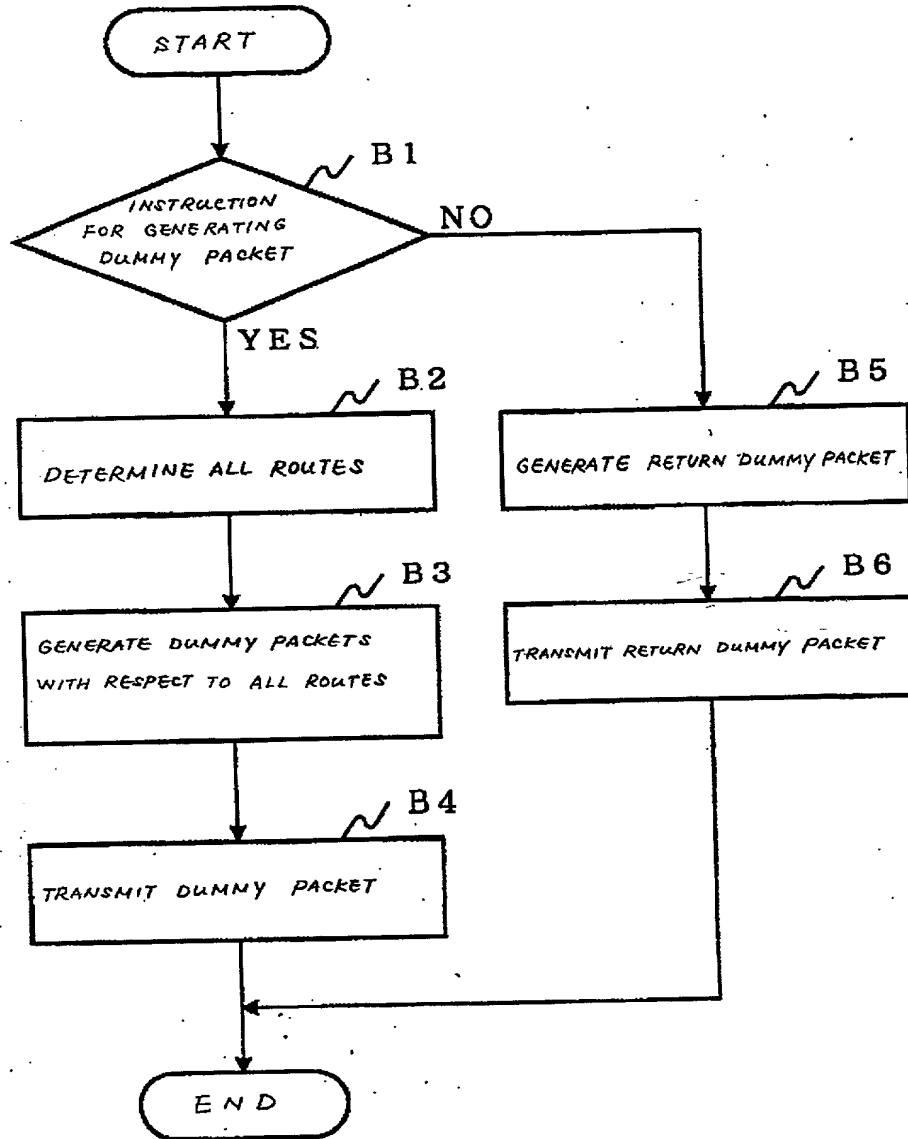


FIG. 4

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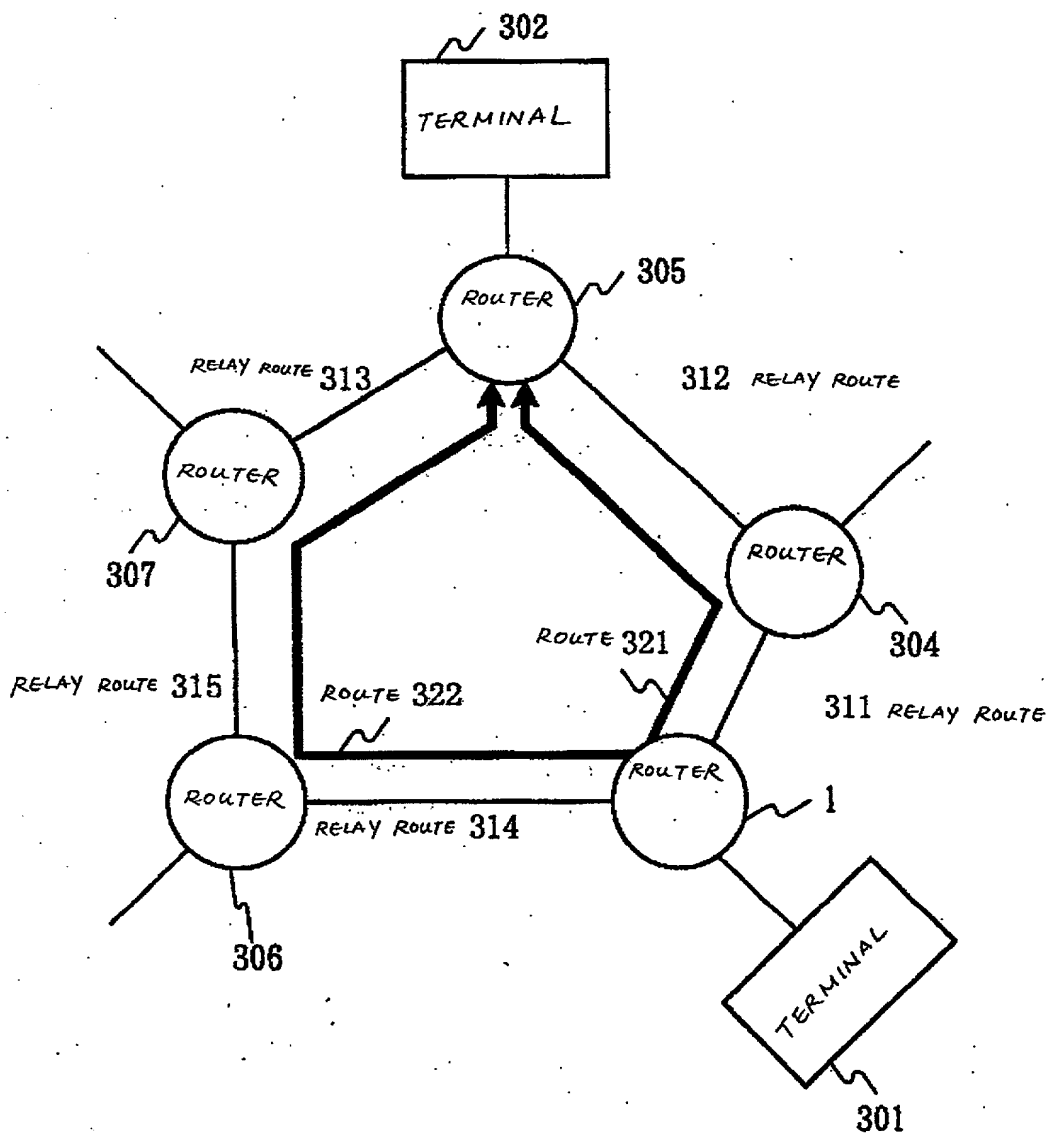


FIG. 5

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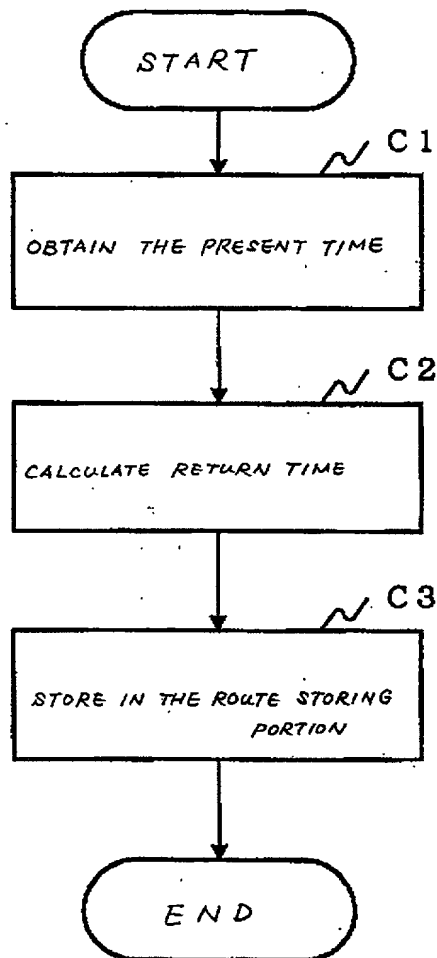


FIG. 6

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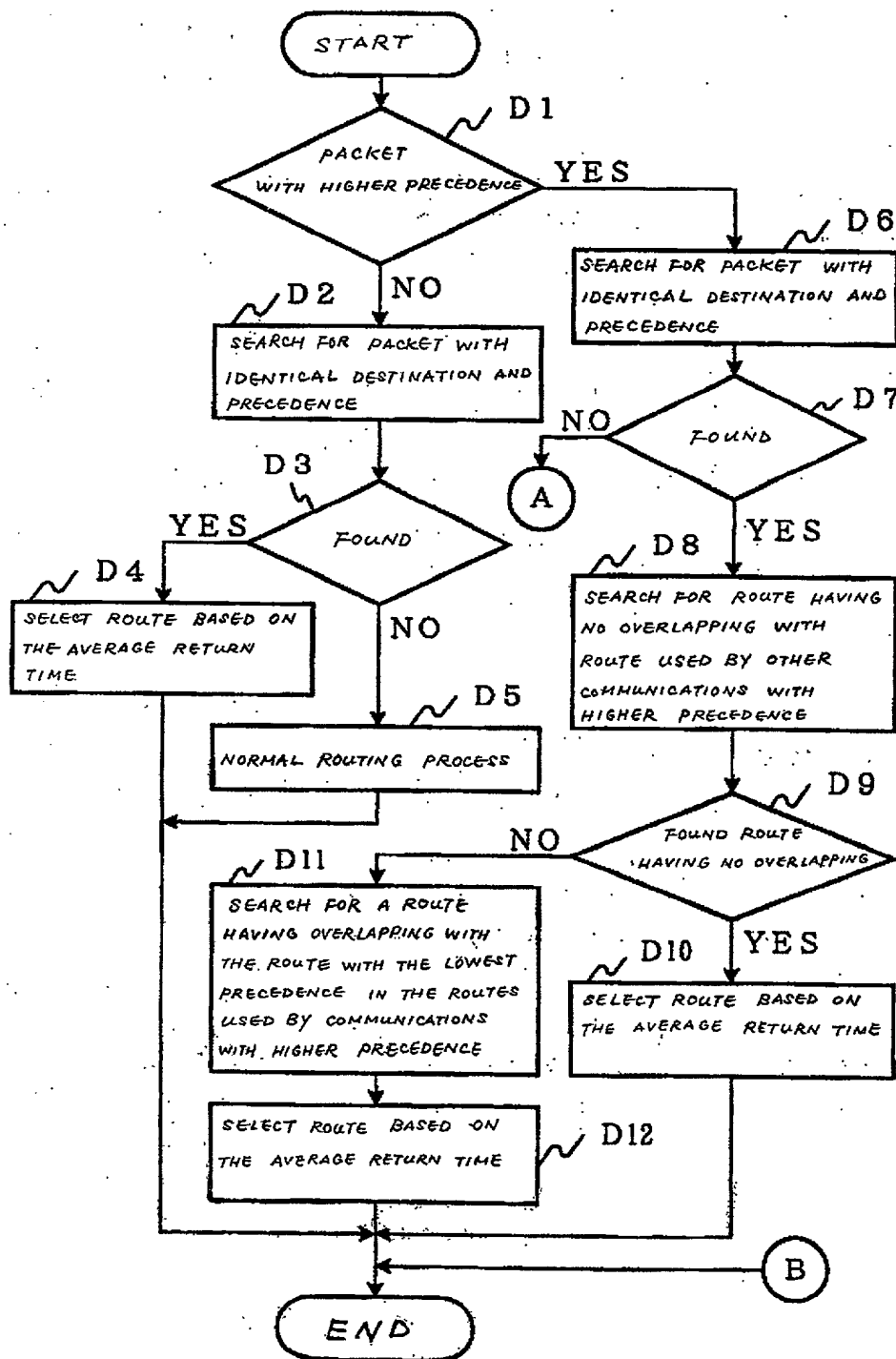


FIG. 7

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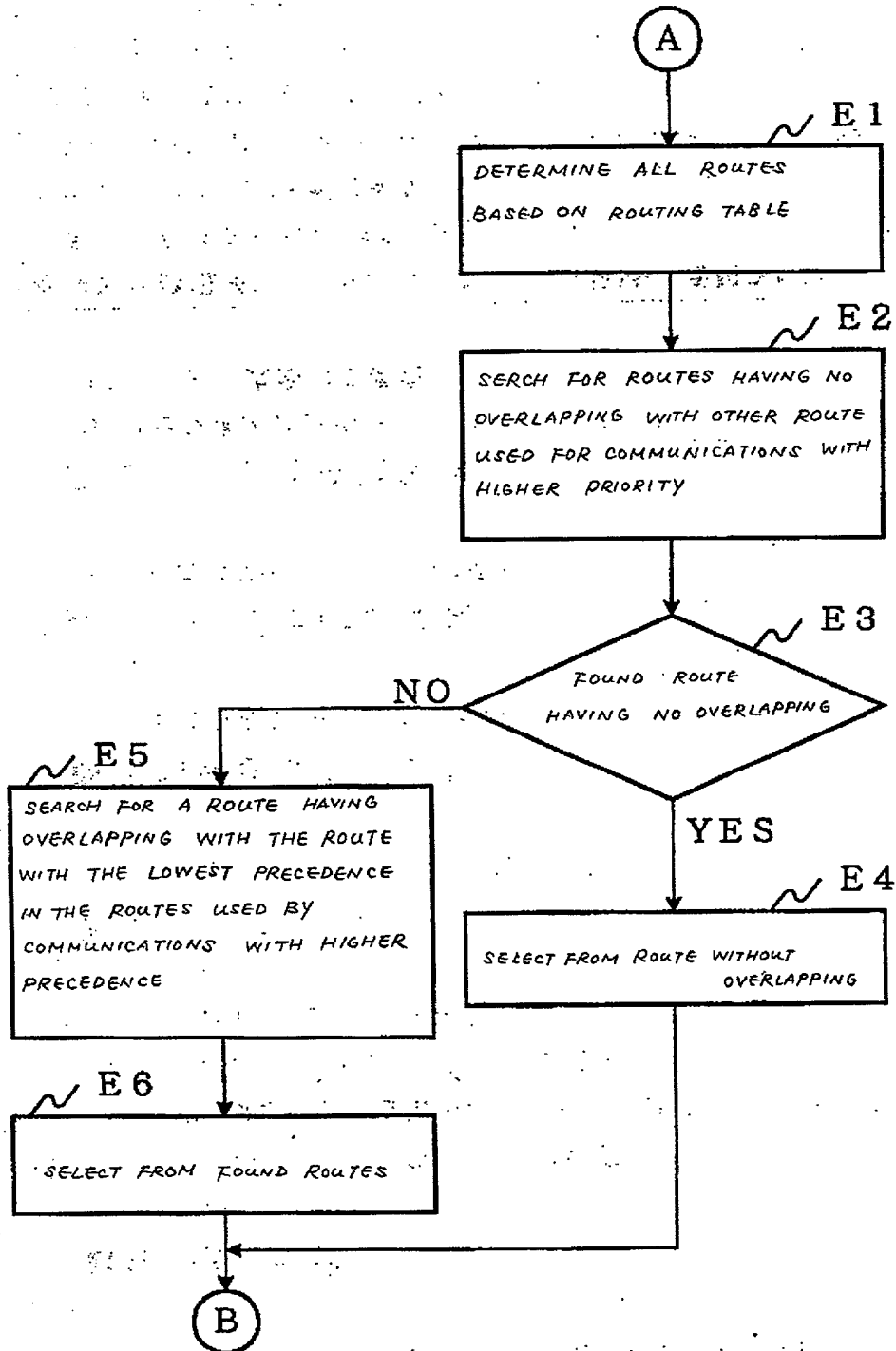


FIG. 8

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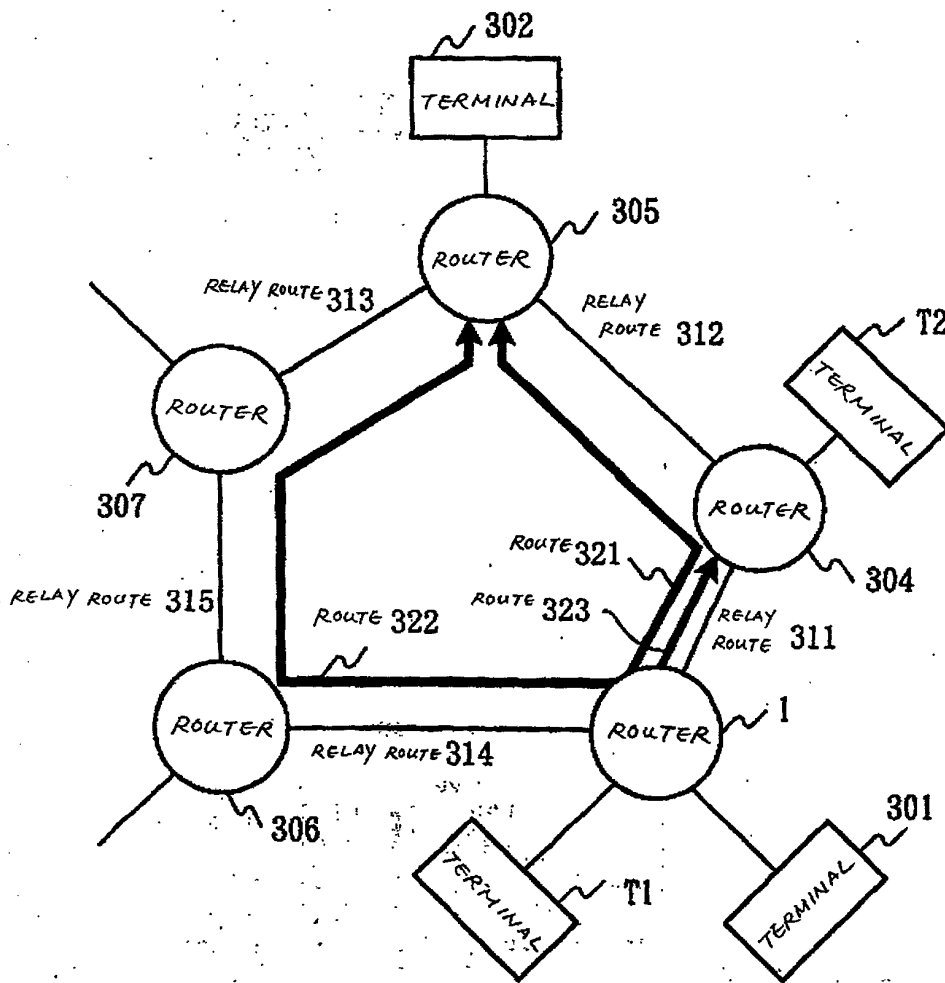


FIG. 9